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PATENT  
Att'y Dkt.: 2207/10608

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MAIYURAN et al

Serial No.: 09/749,750

Filed: December 28, 2000

For: LOW POWER CACHE ARCHITECTURE

Assignee: Intel Corporation

Examiner: S.C. Elmore

Art Unit: 2186

Customer No.: 23838

**RESPONSE TO OFFICE ACTION**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RECEIVED**

AUG 04 2004

Technology Center 2100

Responsive to the Office Action, dated April 30, 2004, kindly amend the application as follows: